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Inventor: Arup Bhattacharyya

Title: High Performance Three-Dimensional TFT-Based CMOS Inverters, and
Computer Systems Utilizing Such Novel CMOS Inverters

Assignee: Micron Technology, Inc.

Serial No.: 10/760,087

Filed: January 14, 2004

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**PURSUANT TO 37 C.F.R. §§ 1.56, 1.97 AND 1.98**

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, the Examiner's attention is directed to the reference listed on the attached Form PTO-1449 and a copy of which is attached. No admission is made regarding whether the submitted reference is prior art.

This Supplemental Information Disclosure Statement is being filed within three months of the filing date of the application or before the mailing of a first Office Action, whichever occurs last. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this information disclosure statement, please charge the fee specified under 37 C.F.R. §1.17(p) to Deposit Account No. 23-0925. Please credit Deposit Account No. 23-0925 with any overpayment of the above fee.

Citation of this reference is respectfully requested.

Respectfully submitted,

Date: 7/8/04Attorney: David G. Latwesen

David G. Latwesen, Ph.D.
Reg. No. 38,533
Wells St. John P.S.

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			ATTY. DOCKET NO. M122-2473		SERIAL NO. 10/760,087	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)					APPLICANT Anup Bhattacharyya			
					FILING DATE January 14, 2004		GROUP 2817	
U.S. PATENT DOCUMENTS								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AA	6,429,085 B1	08/02	Plinter				
	AB							
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	AG							
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FOREIGN PATENT DOCUMENTS								
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							Yes	No
	AM							
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OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)								
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<small>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 809; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</small>								

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Inventor Arup Bhattacharyya
Assignee Micron Technology, Inc.
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Examiner Unknown
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Art Unit 2817
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF FACSIMILE TRANSMISSION UNDER 37 CFR 1.8

I hereby certify that the following papers are being facsimile transmitted to the Patent and Trademark Office at (703) 872-9306 on the date shown below:

1. Certificate of Facsimile Transmission;
2. Supplemental Information Disclosure Statement with PTO-1449;
3. Copy of cited reference.

Dated: July 8, 2004

By: JoAnne O'Hare
JoAnne O'Hare
WELLS ST. JOHN P.S.
Telephone (509) 624-4276

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